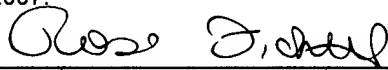




Attorney Docket No. 0756-2330

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:	)	Group Art Unit: 2823
Shunpei YAMAZAKI et al.	)	Examiner: Brook Kebede
Serial No. 09/894,125	)	<u>CERTIFICATE OF MAILING</u>
Filed: June 29, 2001	)	I hereby certify that this correspondence is
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SAME	)	2007.
	)	


SUBMISSION OF CORRECTED FORMAL DRAWINGS

Honorable Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the Notice of Allowability dated June 4, 2007, submitted herewith are 4 sheets of corrected Formal Drawings consisting of Figures 6-12.

Respectfully submitted,

  
Eric J. Robinson  
Reg. No. 38,285

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